

pandoc-imagineによるイメージ生成のサンプル文書

立川察理

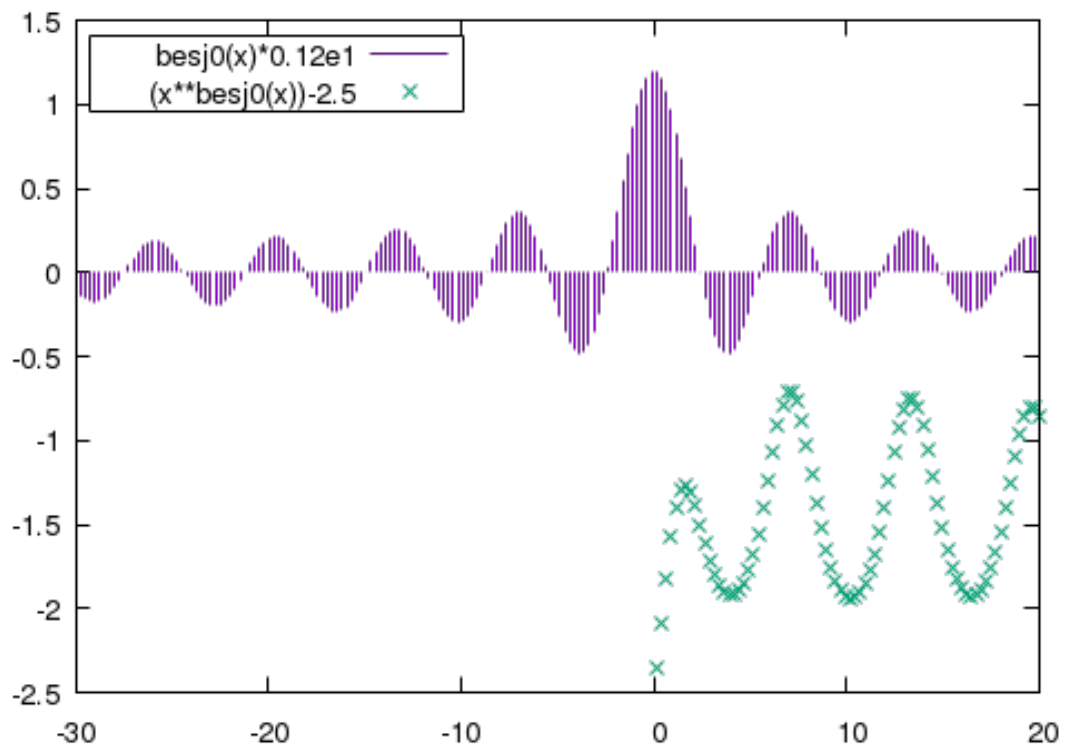
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gnuplot

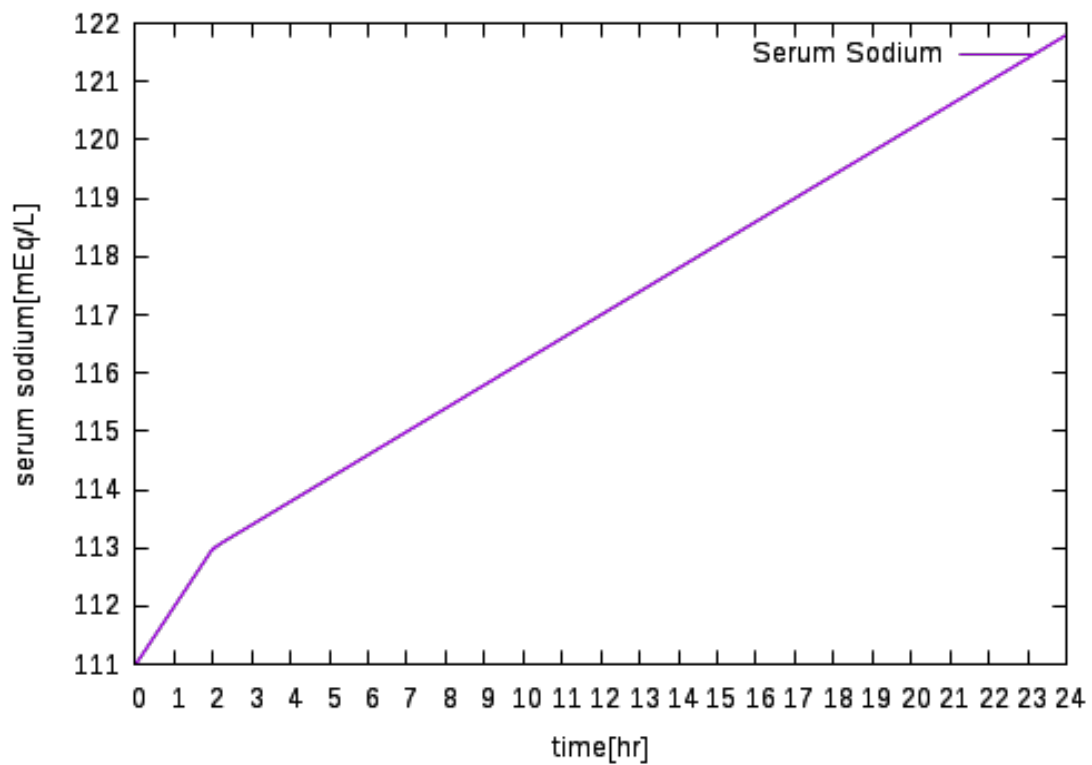
```
```{.gnuplot im_out="fcb,img"}
set terminal pngcairo transparent enhanced font "arial,10" fontscale 1.0 size 500, 350
set key inside left top vertical Right noreverse enhanced autotitles box linetype -1 linewidth 1.
set samples 200, 200
plot [-30:20] besj0(x)*0.12e1 with impulses, (x**besj0(x))-2.5 with points
```
```



```

```{.gnuplot im_out="fcb,img"}
set terminal pngcairo transparent enhanced font "arial,10" fontsize 1.0 size 500, 350
set multiplot
na(x) = x + 111
na2(x) = 0.4 * (x -2) + 113
set xlabel "time[hr]"
set xtics 1
set ylabel "serum sodium[mEq/L]"
set ytics 1
set xrange [0:24]
plot [0:24] x <= 2 ? na(x) : na2(x) title "Serum Sodium"
set samples 5000
replot
```

```



matplotlib

```

```{.shebang im_out="fcb,img" caption="Created by Matplotlib"}
#!/usr/bin/env python

import sys
import numpy as np
import matplotlib as mpl
mpl.use('Agg')
import matplotlib.pyplot as plt

t = np.arange(0.0, 2.0, 0.01)
s = 1 + np.sin(2*np.pi*t)
plt.plot(t, s)

plt.xlabel('time (s)')
plt.ylabel('voltage (mV)')
plt.title('A simple plot')
plt.grid(True)
plt.savefig(sys.argv[-1])
```

```

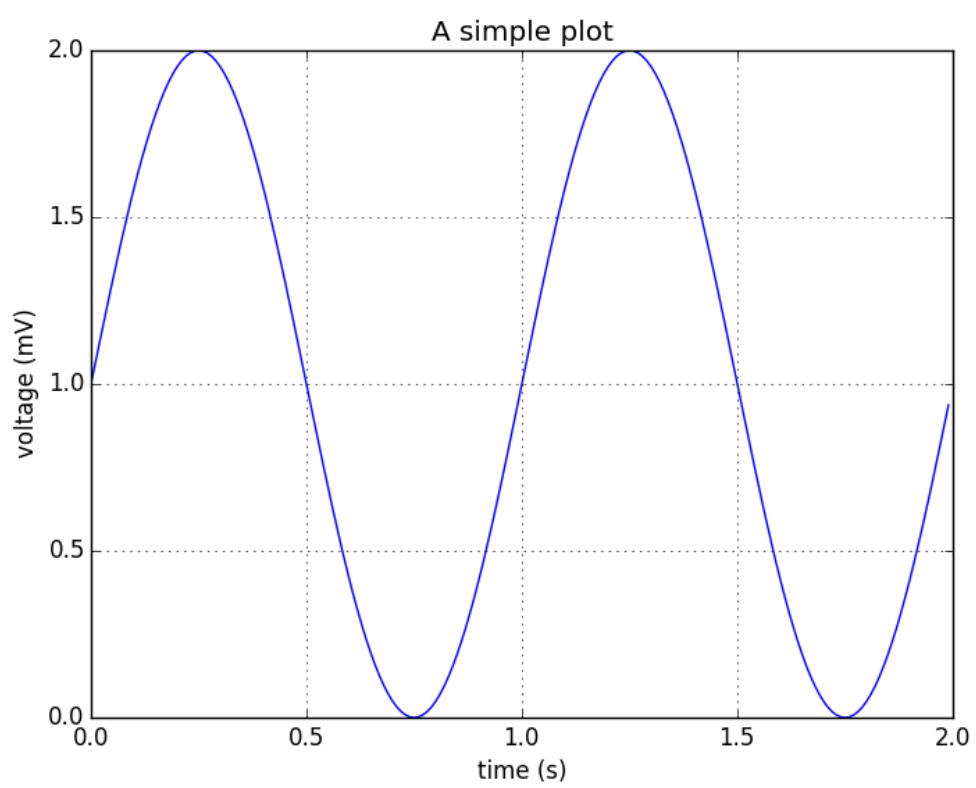


图 1: Created by Matplotlib

Gle

```
```{.gle im_out="fcb,img" caption="Created by GLE"}
! An H-Bridge

size 13 11
include "electronics.gle"

set lwidth 0.05 cap round font psh

! Draw a grid if the line below is uncommented
drawgrid 1

! Top left of diagram
amove 2.0 9.0

! Battery leg
gsave
rline 0 -0.5
cell_v "E_1"
rline 0 -3.5
rline 5 0
rresistor_h R_4
grestore

rresistor_h R_1

gsave
rresistor_v R_2
cell_v "E_2"
grestore

rline 5 0
rresistor_v R_3
rline 0 -4
...

```{.gle im_out="fcb,img" caption="Created by GLE"}
size 4.5 3.5

include "electronics.gle"

sub overline str$ dy
  gsave
```

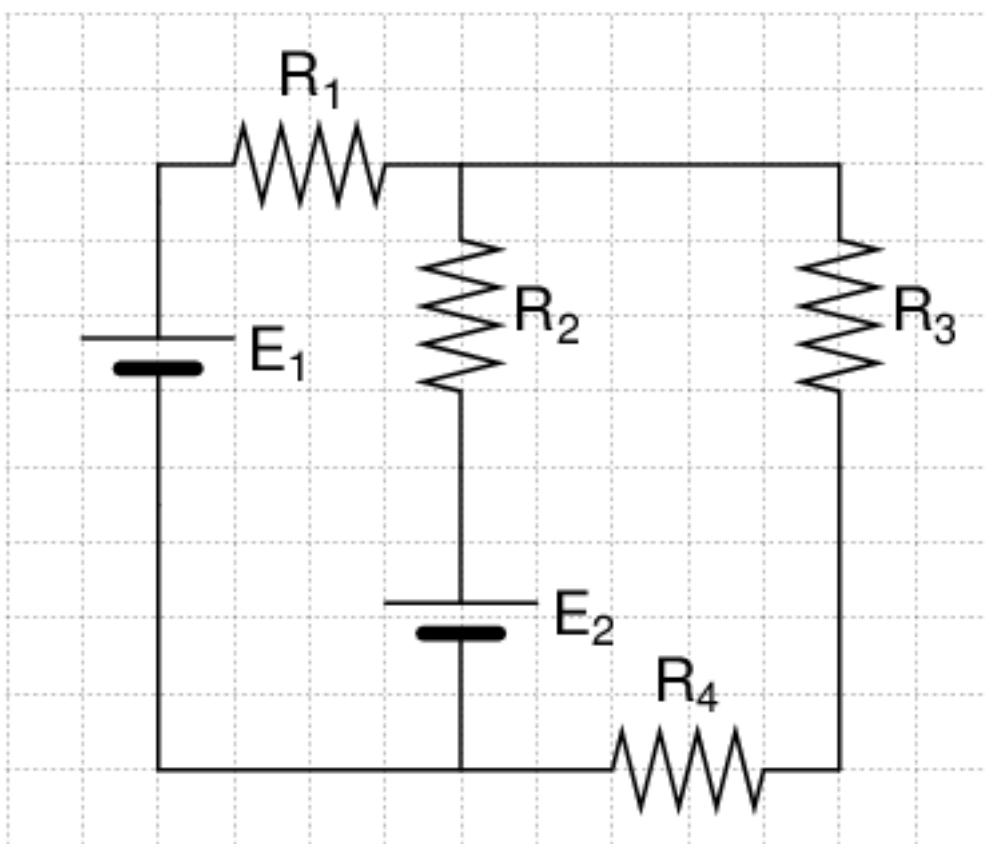


图 2: Created by GLE

```

begin name ov
  write str$
end name
amove pointx(ov.tl) pointy(ov.tl)+dy
aline pointx(ov.tr) pointy(ov.tr)+dy
grestore
end sub

begin scale 0.25 0.25

set lwidth 0.05 hei 1.2 font rm cap round

! Radius of end terminals
termrad = 0.25; x0 = 4.7

! Reset input
amove x0 2
rmove -termrad 0
circle termrad
rmove -2*termrad 0
set just RC color red
text RESET
set color black
rmove 3*termrad 0
rline 2 0

! Set input
amove x0 12
rmove -termrad 0
circle termrad
rmove -2*termrad 0
set just RC color red
text SET
set color black
rmove 3*termrad 0
rline 2 0

! Top NAND Gate
@nand
rmove 0 -2
rline 0 -2
rmove 0 -2
rline 0 -2

```

```

! Bottom NAND Gate
@nand

! Interconnections
rmov 6 -1
rline 1 0
@connection
rline 0 2
rline -7 3
rmov 0 -2
rline 7 3
rline 0 2
@connection
rline -1 0
rmov 1 0

! Top Output
rline 2 0
rmov termrad 0
circle termrad
rmov 2*termrad 0
set just LC color red
text Q
set color black

! Bottom Output
rmov 0 -8
set color red
@overline Q 0.2
set color black
rmov -2*termrad 0
circle termrad
rmov -termrad 0
rline -2 0

end scale
...

```

graphviz

```

...{im_prg="dot" im_opt="-Gsize=4,1.5" caption="FSM layout by dot" im_out="fcb,img"}

```

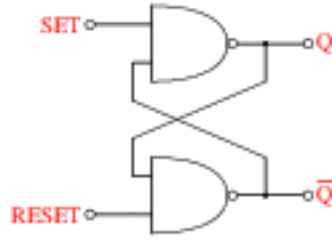


图 3: Created by GLE

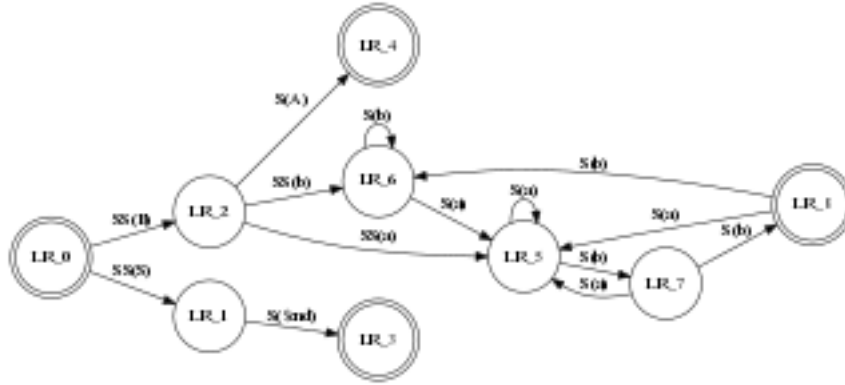


图 4: FSM layout by dot

```

digraph finite_state_machine {
    rankdir=LR;
    size="6,3"
    node [shape = doublecircle]; LR_0 LR_3 LR_4 LR_8;
    node [shape = circle];
    LR_0 -> LR_2 [ label = "SS(B)" ];
    LR_0 -> LR_1 [ label = "SS(S)" ];
    LR_1 -> LR_3 [ label = "S($end)" ];
    LR_2 -> LR_6 [ label = "SS(b)" ];
    LR_2 -> LR_5 [ label = "SS(a)" ];
    LR_2 -> LR_4 [ label = "S(A)" ];
    LR_5 -> LR_7 [ label = "S(b)" ];
    LR_5 -> LR_5 [ label = "S(a)" ];
    LR_6 -> LR_6 [ label = "S(b)" ];
    LR_6 -> LR_5 [ label = "S(a)" ];
    LR_7 -> LR_8 [ label = "S(b)" ];
    LR_7 -> LR_5 [ label = "S(a)" ];
    LR_8 -> LR_6 [ label = "S(b)" ];
    LR_8 -> LR_5 [ label = "S(a)" ];
}

```

mscgen

```
```{.mscgen im_out="fcb,img"}
msc {
 hscale="1.3", arcgradient = "8";

 a [label="Client"],b [label="Server"];

 a->b [label="data1"];
 a-xb [label="data2"];
 a->b [label="data3"];
 a<=b [label="ack1, nack2"];
 a->b [label="data2", arcskip="1"];
 |||;
 a<=b [label="ack3"];
 |||;
}
```
```

